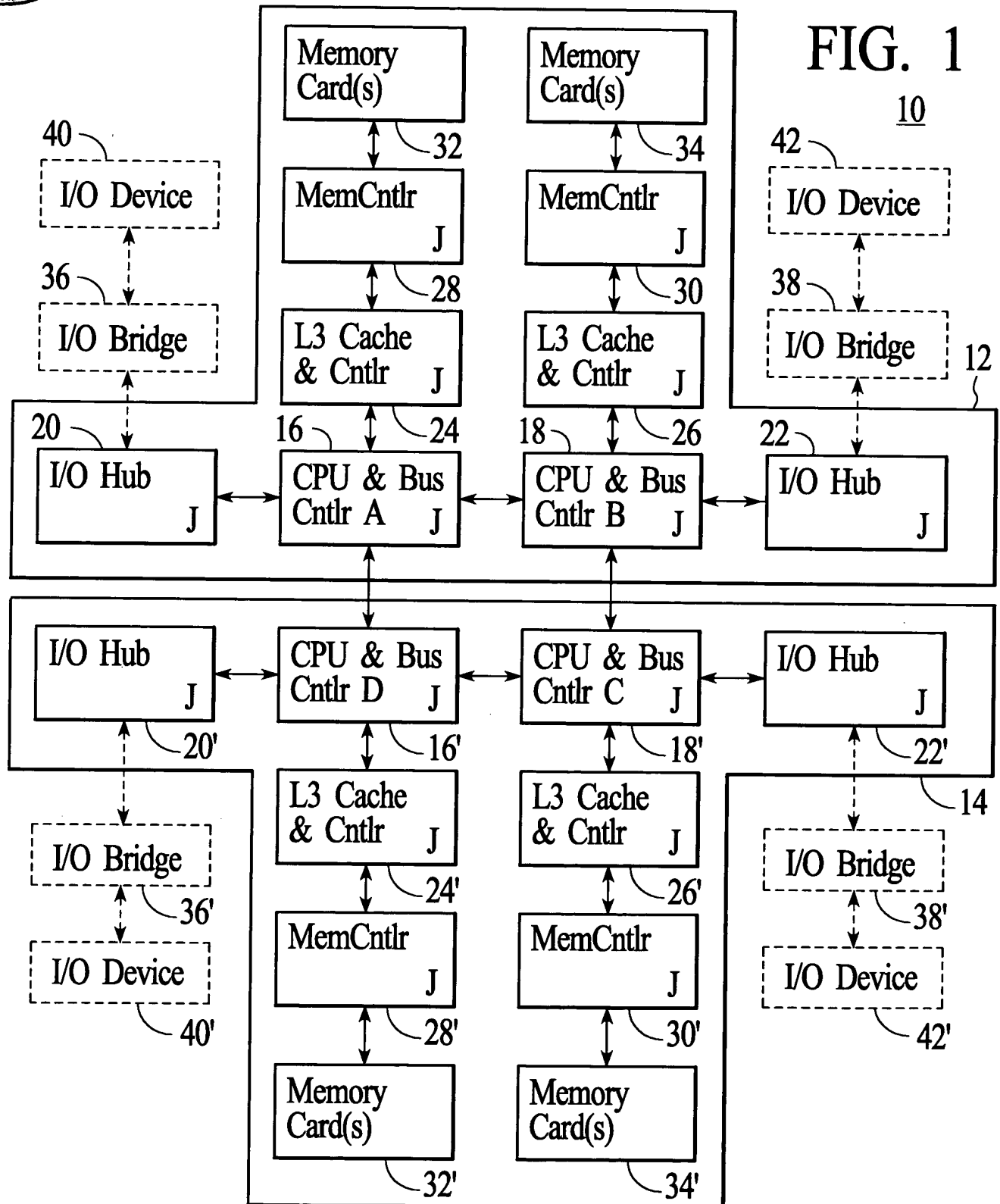


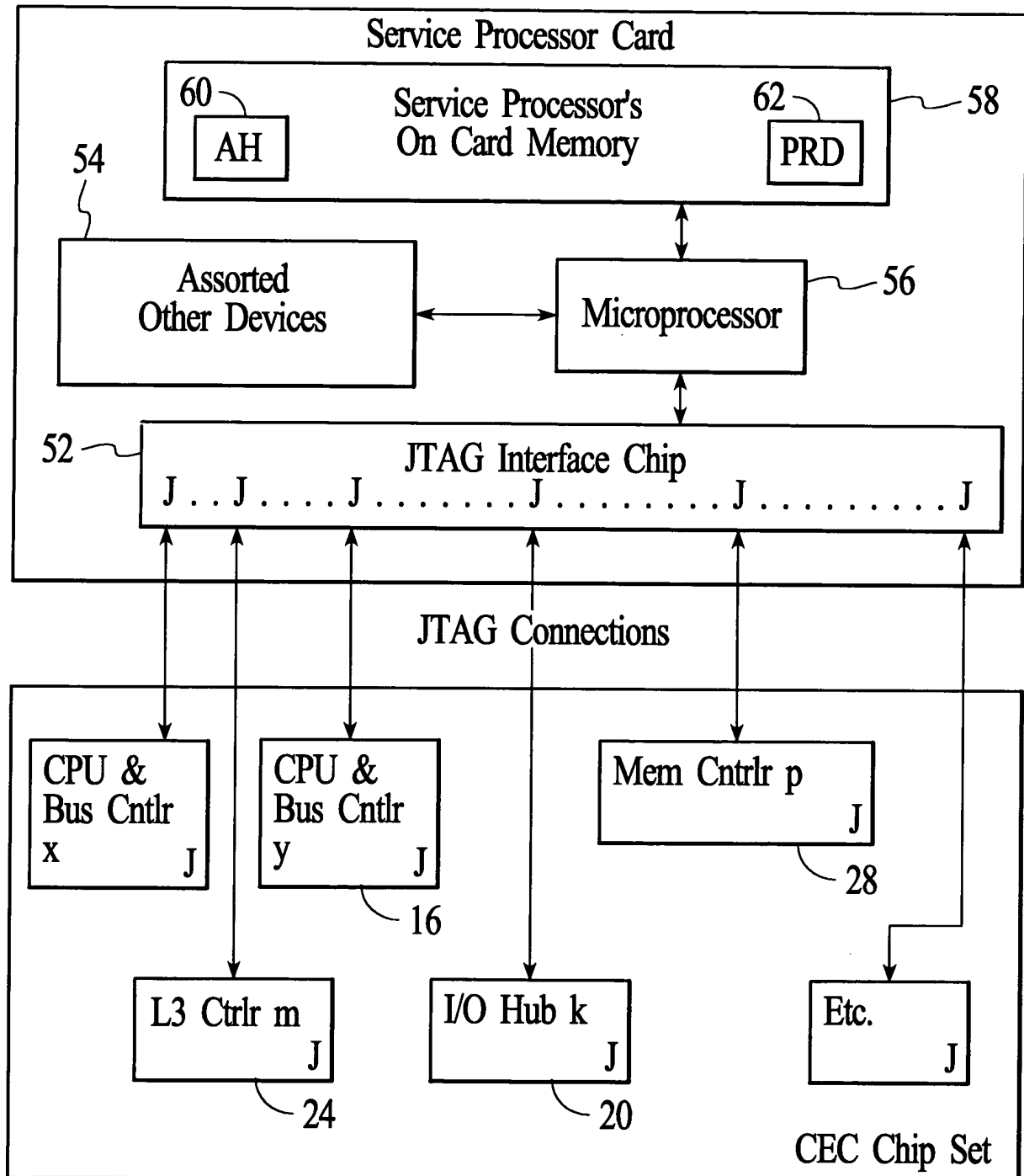
FIG. 1



J = chip has serial jtag connection to Service Processor HW

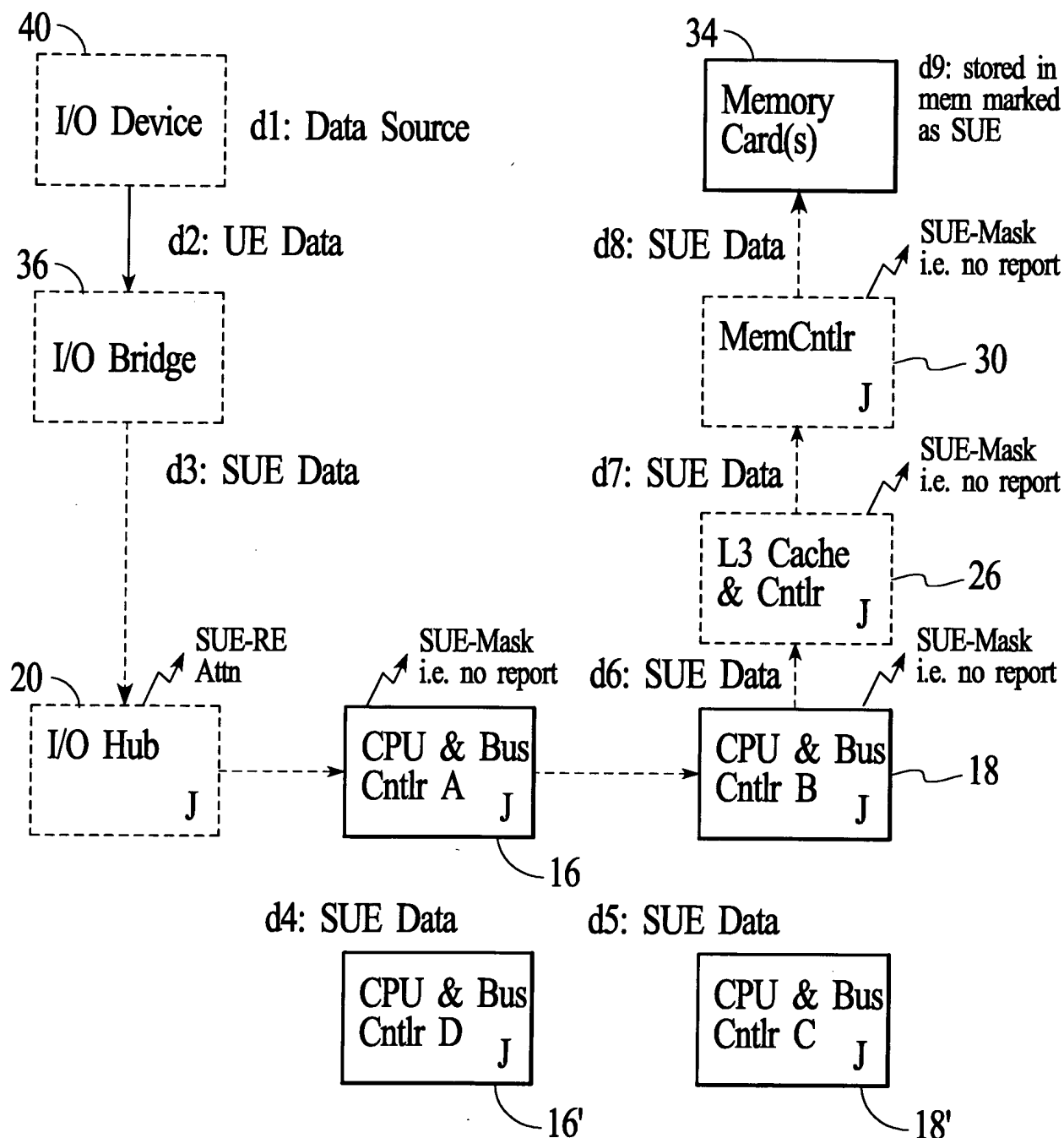
□ dotted box indicates remote chip or device inaccessible to SP via jtag

↔ dotted line indicates connections to and between remote chips & devices



J = JTAG serial interface between Service Processor & chips in CEC

FIG. 3A

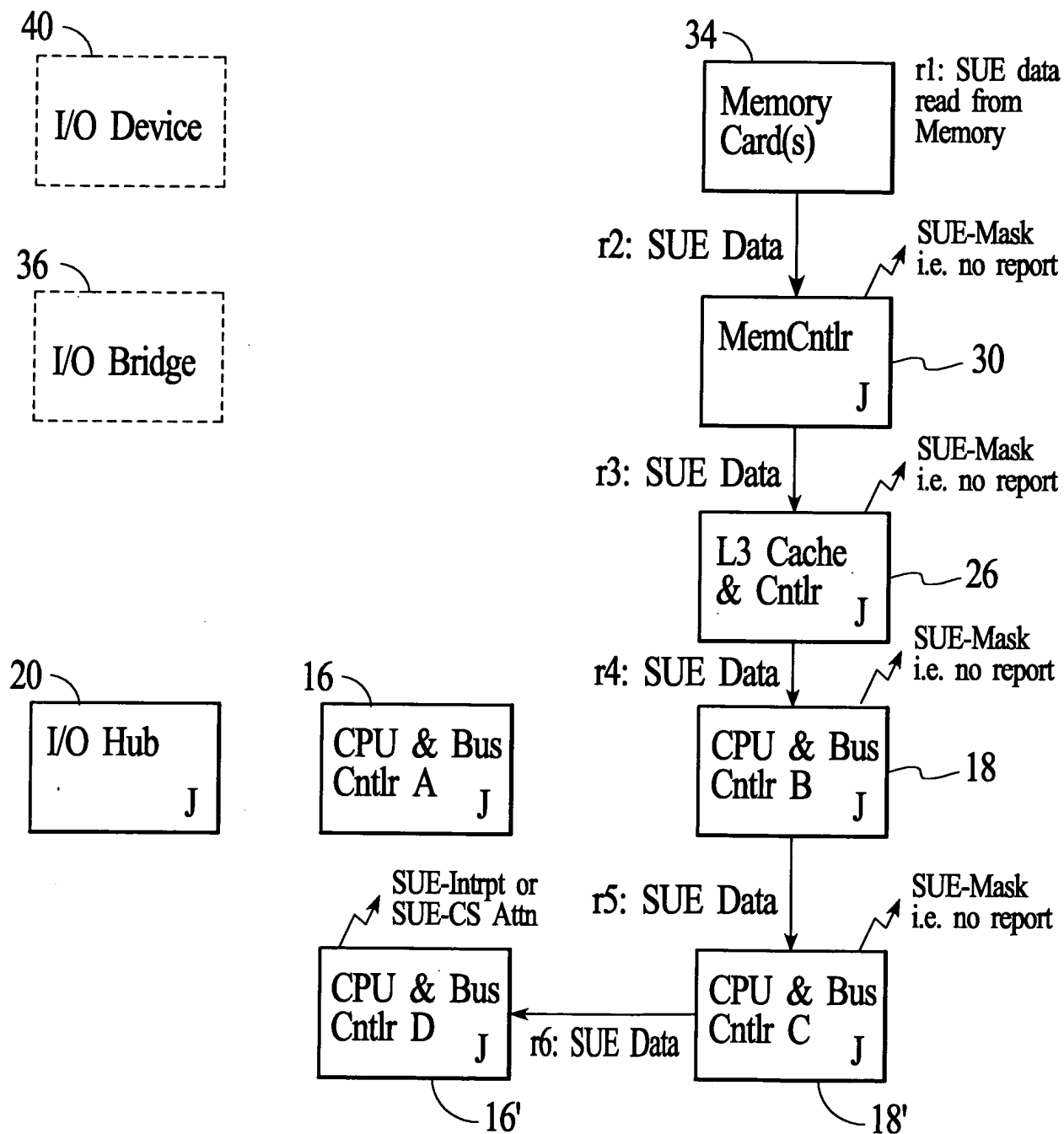


J = chip has serial jtag connection to Service Processor HW

□ dotted box indicates remote chip or device inaccessible to SP via jtag

---> dotted line indicates connections to and between remote chips & devices

FIG. 3B



J = chip has serial jtag connection to Service Processor HW

□ dotted box indicates remote chip or device inaccessible to SP via jtag

←--> dotted line indicates connections to and between remote chips & devices



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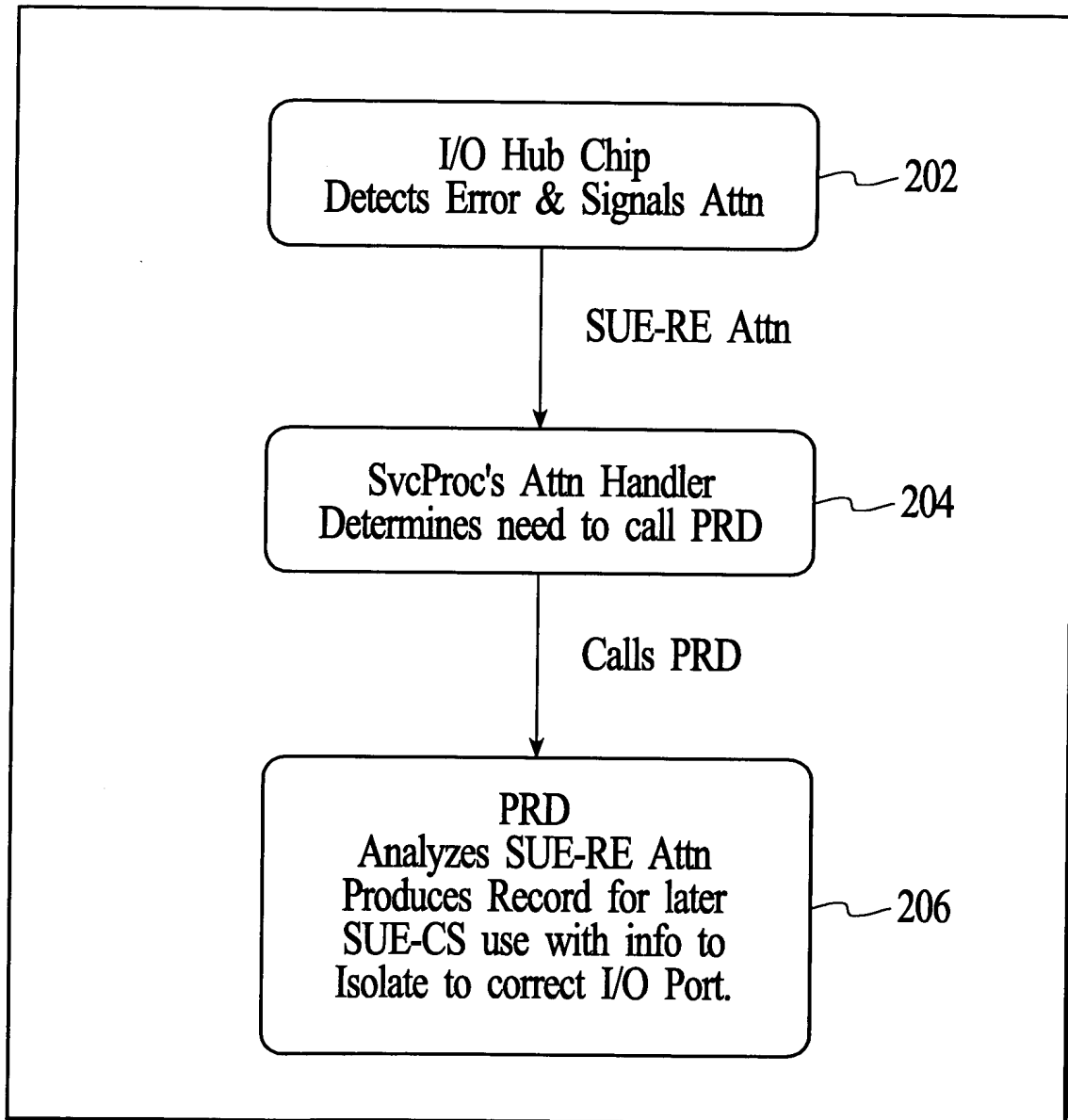


FIG. 4



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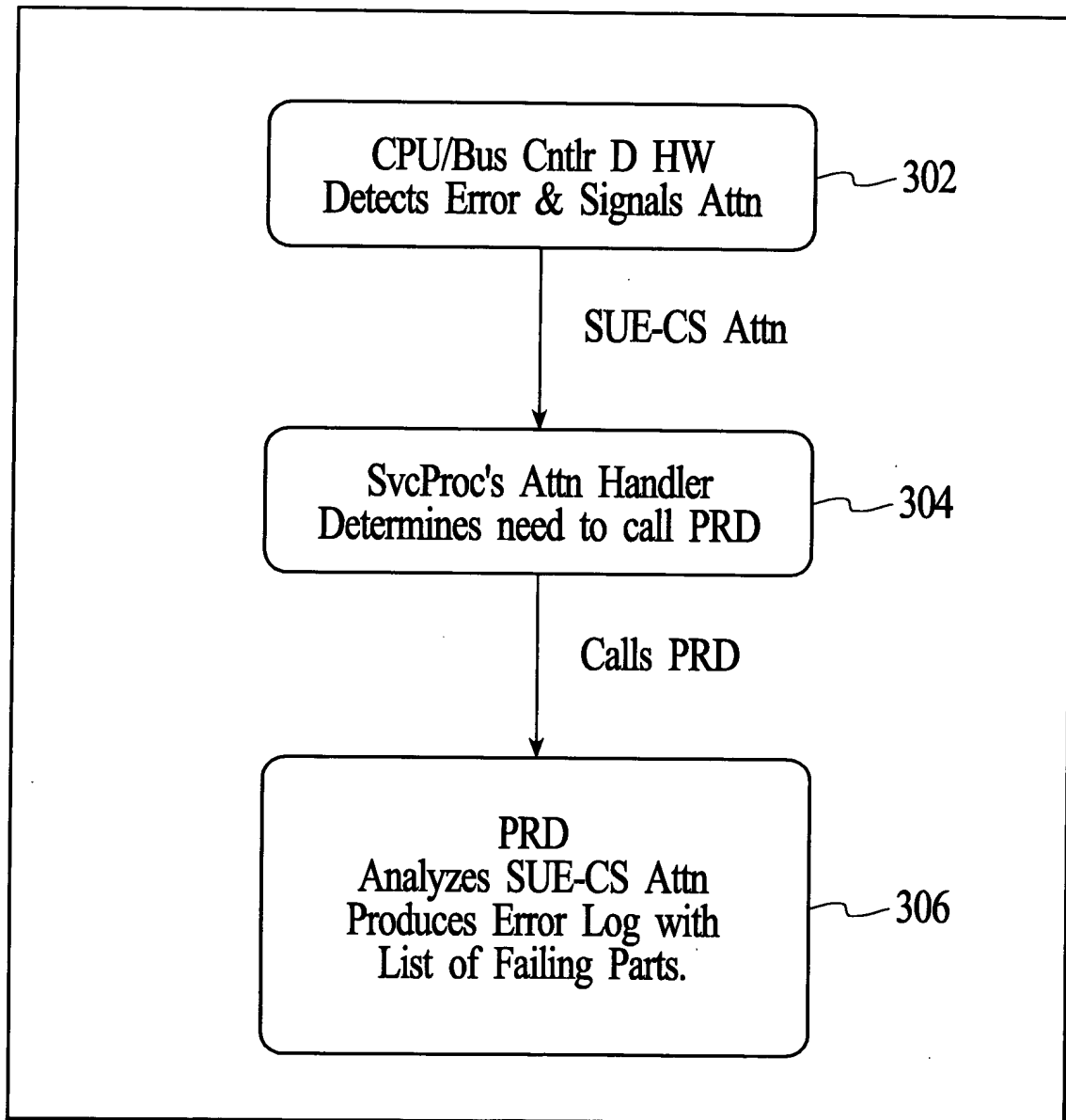
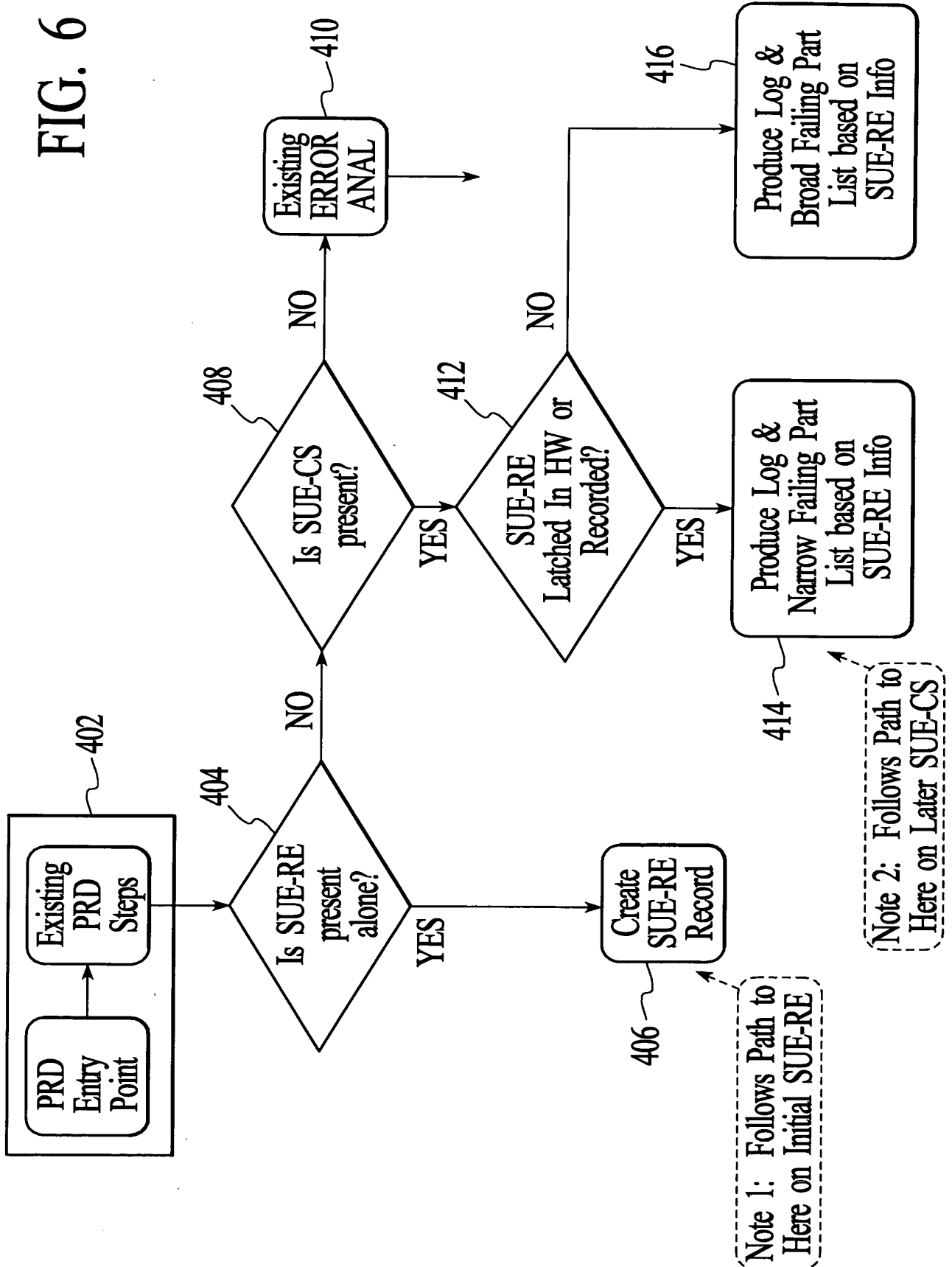


FIG. 5

FIG. 6





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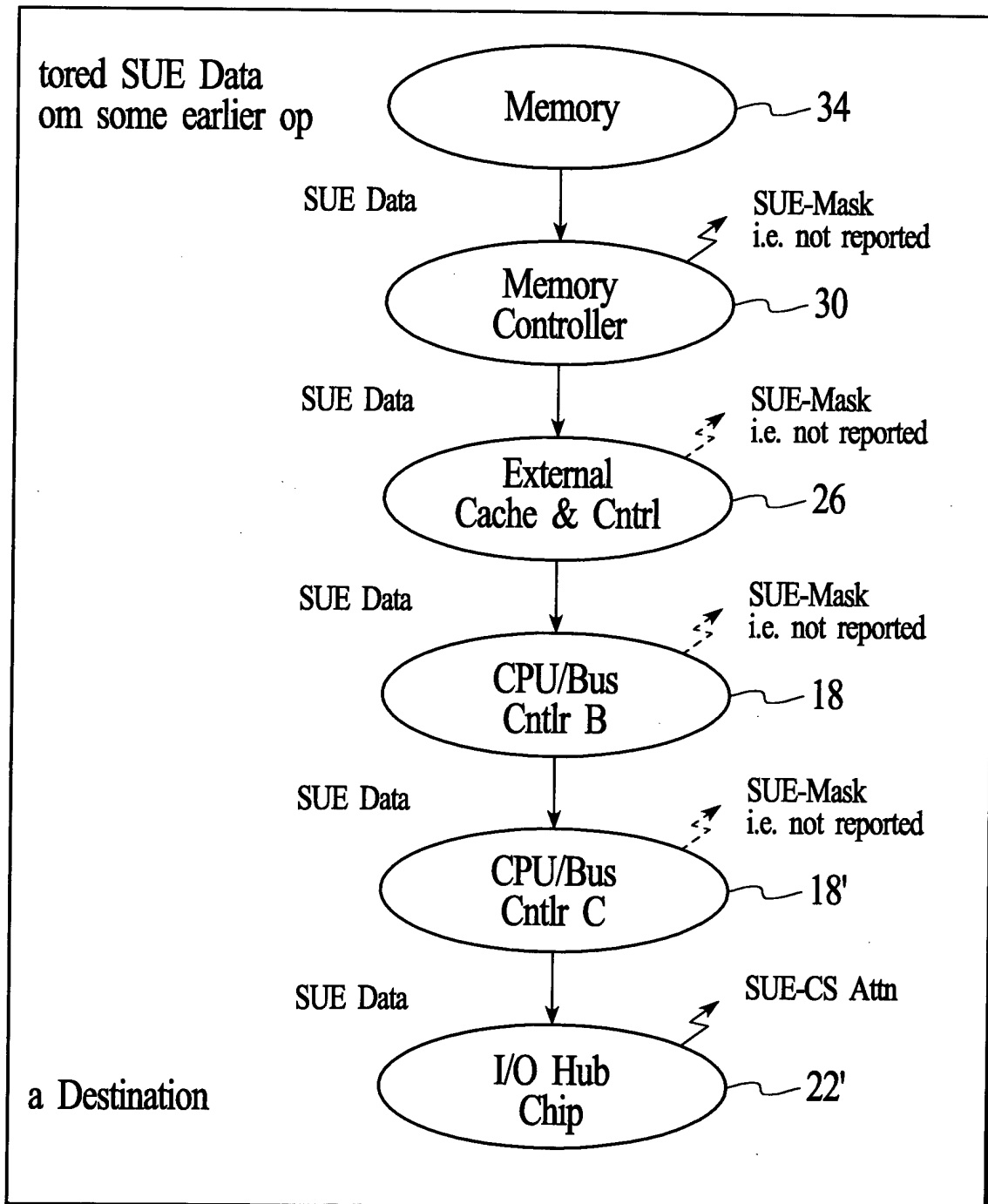


FIG. 7